



Tower Semiconductor to Participate in the 52nd Annual JP Morgan Global Technology, Media, and Communications Conference

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MIGDAL HAEMEK, Israel, May 01, 2024 – Tower Semiconductor (NASDAQ/TASE: TSEM), the leading foundry of high-value analog semiconductor solutions, today announced that its Chief Executive Officer, Russell Ellwanger, will participate in the 52nd Annual JP Morgan Global Technology, Media, and Communications Conference on Monday, May 20 and Tuesday, May 21.

The conference will take place at The Westin Boston Seaport District Hotel in Boston. Mr. Ellwanger will participate in a fireside chat on Monday, May 20 at 2:30 pm ET. The presentation will be simultaneously webcast and will be accessible from the investor relations section of Tower Semiconductor's website at <https://ir.towersemi.com/>.

At the conference, there will be an opportunity for investors to meet one-on-one with management. Interested investors should contact the conference organizers or email the investor relations team at towersemi@kcsa.com.

About Tower Semiconductor

Tower Semiconductor Ltd. (NASDAQ/TASE: TSEM), the leading foundry of high-value analog semiconductor solutions, provides technology, development, and process platforms for its customers in growing markets such as consumer, industrial, automotive, mobile, infrastructure, medical and aerospace and defense. Tower Semiconductor focuses on creating a positive and sustainable impact on the world through long-term partnerships and its advanced and innovative analog technology offering, comprised of a broad range of customizable process platforms such as SiGe, BiCMOS, mixed-signal/CMOS, RF CMOS, CMOS image sensor, non-imaging sensors, displays, integrated power management (BCD and 700V), photonics, and MEMS. Tower Semiconductor also provides world-class design enablement for a quick and accurate design cycle as well as process transfer services including development, transfer, and optimization, to IDMs and fabless companies. To provide multi-fab sourcing and extended capacity for its customers, Tower Semiconductor owns two facilities in Israel (150mm and 200mm), two in the U.S. (200mm), two in Japan (200mm and 300mm) which it owns through its 51% holdings in TPSCo, shares a 300mm facility in Agrate, Italy, with ST as well as has access to a 300mm capacity corridor in Intel's New Mexico factory. For more information, please visit: www.towersemi.com.

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Attachment

- [Tower Semi JP Morgan Conference Announcement PR](#)



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